Logical Computation on QLDPC Codes through Surgery

With Case Study on Bivariate Bicycle codes

Andrew Cross, Zhiyang He (Sunny), Tomas Jochym-O'Connor, Patrick Rall, Esha Swaroop, Dominic Williamson, Theodore Yoder



Outline

- Background and Motivation
 - $\circ \quad Quantum \ LDPC \ Codes$
 - Code Surgery Methods
- Auxiliary Graph Surgery on QLDPC Codes
 - Graph Desiderata
 - Universal Adapter for Joint-measurements
- Case Study: [[144,12,12]] Bivariate bicyclic code

Basics of QEC

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One of the most promising codes: Surface Code.

- 1. Built on a 2D lattice of qubits.
- 2. Parameters $[n = 2L^2, k = 1, d = L]$.
- 3. Experimental demonstration of subthreshold scaling by Google*.

Challenge:

1. Significant space overhead (~1000x)!



Quantum LDPC Codes: the Tradeoff

Quantum Low-Density Parity-Check (LDPC) Codes: stabilizers of O(1) weight, qubits in O(1) stabilizers. Better encoding rate than surface code!

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Practically: many codes with nice parameters, such as the quasi-cyclic lifted product codes and IBM's [n = 144, k = 12, d = 12] Bivariate Bicycle Code.*

Theoretically: Asymptotically good codes with $k, d = O(n).^{**}$



B) Tanner Graph of the [[144,12,12]] Bivariate Bicycle Code

^{*} High-threshold and low-overhead fault-tolerant quantum memory [Bravyi et al 2023]

^{**} Asymptotically Good Quantum and Locally Testable Classical LDPC Codes [Panteleev Kalachev 2021]

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This is the central problem in the study of QLDPC codes.



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Logical Computation on QLDPC Codes through Surgery

This talk: QLDPC surgery is a method of logical computation that is fault-tolerant, addressable, universal, and low-overhead. Work done collectively in three papers.

1. Improved QLDPC Surgery: Logical Measurements and Bridging Codes. Andrew Cross, **Zhiyang He**, Patrick Rall, Theodore Yoder. 2407.18393

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Other related works: 2407.09423 (Cowtan), 2407.18490 (Xu et al.), 2408.01339 (Zhang, Li), 2410.02753 (Ide et al.).

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Logical Measurement and Lattice Surgery

Pauli-based computation: Pauli measurements on logical qubits + magic states = universal computation. Logical measurements on surface codes: lattice surgery.

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Product of red X-checks = $X_L \otimes X_L$ – obtain logical measurement result by measuring new stabilizers.

* Figure from Entangling logical qubits with lattice surgery [Erhard et al 2020]. 5

Tanner graph of code

Shorthand form





















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Advantage: applicable to any QLDPC code, Issue: space overhead ~ O(d²), similar to surface code!

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OK... what about code distance? \bigvee



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Solution: if $\boldsymbol{\mathcal{G}}$ is expanding (large Cheeger constant), then $\boldsymbol{\mathcal{U}}$ has large support $\boldsymbol{\mathcal{U}}\boldsymbol{\mathrm{G}}^{\mathsf{T}}$ in $\boldsymbol{\mathcal{E}}$.



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Other ways to reduce distance? Not if we measure the cycles **C**.

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Ingredient #1: choose a randomly constructed constant-degree expander graph.



* stick figure credit: xkcd.com 17



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 $\Leftrightarrow \text{ original X stabilizers anti-commute with an} \\ \text{even number of new vertex Z stabilizers } \textit{U}.$



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To ensure N is sparse,



- Each edge isn't in too many cycles
- Each cycle* isn't too long.

*element of the cycle basis

Ensuring the cycle basis of graph is sparse :

• Each edge appears only in O(1) cycle basis elements

Ingredient #3: a notion of "decongesting" cycles in a graph



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A cycle basis to begin with [Freedman Hastings 2020]

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A cycle basis to begin with [Freedman Hastings 2020]



Cycles = X-checks Edges = qubits Input: graph **G** with O(1) vertex degree Output: a cycle basis s.t. each cycle overlaps with at most O(log³ (|V|)) cycles.

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Desiderata 4: Sparse cycle basis for graph **G**

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Desiderata 4: Sparse cycle basis for graph **G**

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Also "cellulate" long cycles into smaller cycles

• Each cycle basis element has O(1) edges

Graph Desiderata



We want:

- 1. Code should preserve distance of original code.
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Graph $\boldsymbol{\mathcal{G}}$ should have the following properties:

- 1. **G** is expanding; \checkmark
- 2. All vertices have O(1) degree \checkmark
- 3. Short perfect matchings on 𝔅 (For original X checks) ✓
 & each edge is in O(1) matchings. ✓
- 4. **G** has a sparse cycle basis. \checkmark

Overall protocol for auxiliary graph qLDPC surgery



(i) initialize

Overall protocol for auxiliary graph qLDPC surgery



(i) initialize

(ii) merge step

Overall protocol for auxiliary graph qLDPC surgery



(i) initialize

(ii) merge step

(iii) split step

In summary: auxiliary graph qLDPC surgery



✓ Applicable to any quantum LDPC code!

In summary: auxiliary graph qLDPC surgery



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Qubit overhead of scheme: O(d log³ (d))

~ O(d) in distance d, upto polylog

In summary: auxiliary graph qLDPC surgery



✓ Applicable to any quantum LDPC code!

Qubit overhead of scheme: O(d log³ (d))

~ O(d) in distance d, upto polylog

 \Rightarrow Significant improvement in overhead from previous scheme for arbitrary quantum LDPC codes, O(d²)





Construct a large **auxiliary graph** on the **entire logical**.





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This would mean exponentially many auxiliary graphs for each of the ~ 4^k logical operators!



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Can we break up the problem?

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New cycles are at most length 8 = new X stabilizers are at most weight 8





Key insight: any tree-like graph is equivalent to a repetition code up to a transformation



SkipTree(G) = T, P

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Significance : These can be arbitrary* logical operators in the LDPC code, they could belong to the same or different codeblocks, or even different quantum codes

A "Universal" way to connect between any two logical operators in quantum LDPC codes

Universal adapter: Connecting different qLDPC codes

- Arbitrary joint-measurements in the same or different codeblocks
- multi-code architectures...

Can be useful for teleportation, transversal gates, magic state factory, code-switching...



Can leverage known symmetries in codes, and implement these gates on logical qubits of other codes.

In Summary

- Addressable gates for multi-qubit code blocks
- A "universal" scheme applicable to arbitrary quantum LDPC codes
- Space overhead is only ~linear which is a quadratic improvement over previous schemes; the LDPC overhead advantage is now beginning to show even for logical gate schemes.
- Can connect arbitrary codes for multi-code architectures with just ~d extra qubits allowing one to leverage unique advantages of different codes, while keeping architecture LDPC.

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Uses 1380 ancilla qubits.

* High-threshold and low-overhead fault-tolerant quantum memory [Bravyi et al 2023]

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Uses 1380 ancilla qubits.

Teleport logical information into a surface code patch to do computation.

New Proposal for Logical Computation


New Proposal for Logical Computation



Uses 103 ancilla qubits, 13x savings.

Use a bridge to perform joint measurement.

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Automorphism gates + logical measurement = Full logical Clifford group!

Promising numerical benchmarking.

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The End

